
Table of Contents

Preface	XI
1. Introduction	1
1.1 Motivation	1
1.2 Car Radio History	5
References	9
2. DSP Based Radio Receiver Architectures	11
2.1 Radio Receiver Architectures	12
2.1.1 Heterodyne and Homodyne Receivers	12
2.1.2 DSP Based Radio Receiver Architectures	14
2.2 ADC Performance Metrics	17
2.2.1 Measures of Resolution	17
2.2.2 Measures of Linearity	18
2.3 Desensitization and Blocking	21
2.4 Image Rejection	24
2.5 Analog Radio Broadcasting	29
2.6 Digital Radio Broadcasting	30
2.7 Integrated Solutions for AM/FM Receivers	32
References	37
3. Continuous-Time $\Sigma\Delta$ Modulation	41
3.1 Basic Principles	42
3.1.1 Oversampling	43
3.1.2 Noise Shaping	46
3.1.3 Anti-alias Filtering	48
3.2 High-Order $\Sigma\Delta$ Modulators	50
3.3 Tonal Behaviour	57
3.3.1 DAC Modulation at $f_s/2$	59
3.3.2 2 nd -Order Non-linearity	61
3.4 Clock Jitter	65
3.4.1 Random Jitter	65
3.4.2 Deterministic Jitter	68
References	69

4. ΣΔ ADC Topologies for Radio Receivers	71
4.1 Lowpass ΣΔ ADCs	72
4.1.1 Feedback Compensation	73
4.1.2 Feedforward Compensation	78
4.1.3 Feedforward and Feedback Compensation	81
4.1.4 Feedback Compensation with Local Feedforward Path	84
4.1.5 Resonators and Local Feedback	87
4.2 IF-to-Baseband ΣΔ ADCs	90
4.3 Quadrature IF-to-Baseband ΣΔ ADCs	93
4.4 Bandpass ΣΔ ADCs	102
4.4.1 Continuous-Time Bandpass ΣΔ Modulators	103
4.4.2 Continuous-Time Resonators	106
4.5 Quadrature Bandpass ΣΔ ADCs	109
4.6 Conclusions	111
References	113
5. IF-to-Baseband ΣΔ ADC for AM/FM/IBOC Receivers	117
5.1 IF-to-Baseband Conversion System	118
5.2 IF Mixer	124
5.2.1 Mixer Linearity	125
5.2.2 Mixer Dynamic Performance	126
5.2.3 Isolated Mixer Topology	132
5.2.4 Mixer Driver	136
5.3 First Integrator	138
5.3.1 Design of the 1 st OTA To Be Used as a CT Integrator	140
5.3.2 Design of the 1 st OTA To Be Used as a SC Integrator	143
5.3.3 1 st OTA Transistor Level Design	149
5.4 High-Order Integrators and Resonators	152
5.5 Feedforward Coefficients and Quantizer	157
5.6 SC Feedback DAC	159
5.7 Experimental Results	163
5.7.1 First Prototype	164
5.7.2 Second Prototype	171
References	176

6. Conclusion	179
6.1 Benchmarking	179
6.2 Economic Feasibility	181
References	183
A. Harmonic Distortion in CT Integrators Using MOSCAPs	187
A.1 Introduction	187
A.2 Harmonic Distortion in Differential G_m -C Integrators	189
A.3 Harmonic Distortion in Differential Feedback Integrators	192
References	195
B. Noise Analysis of CT $\Sigma\Delta$ Modulators with SC Feedback DAC	197
B.1 Introduction	197
B.2 Noise Voltage PSD Across a Switched Capacitor	199
B.3 Input-Referred Thermal Noise Due to the SC Feedback DAC MOS Switches	201
B.3.1 “Direct Noise” Components.....	201
B.3.2 “Sampled-and-Held” Components.....	203
B.3.3 Input-Referred Noise	204
B.4 Input-Referred Thermal Noise Due to the SC Feedback DAC Reference Voltage	205
B.5 Input-Referred Thermal Noise Due to the OpAmp	206
B.6 Conclusion	208
References	208
List of Acronyms.....	209
List of Symbols.....	211
Index	213
About the Authors	217